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Appl. No. 09/602,422 Amdt. dated August 12, 2003 Reply to Office Action of May 23, 2003

Amendments to the Specification

Please amend the second paragraph on page 20 of the Specification as shown.

In one embodiment of the present invention, parameter application module 320 creates an underlying structure list based upon information supplied by a user via user interface module 310 and information provided by expert system module 330. In creating the underlying structure list, the parameter application module 320 interprets the user-entered information and commands and performs the required iterations. Parameter application module 320 interprets the information and performs complex-iterations required to generate an underlying structure list. In one embodiment of the present invention, parameter application module 320 initializes a corresponding command line string for a circuit block descriptions (e.g., a timer description) forwarded by expert system module 330. For example, a parameterizable command line string required to generate the selected block circuit (e.g., a timer) from a hardware description language (e.g., reuse library) is initialized and the operations for each block circuit (e.g., a timer) the user has requested are processed. Parameter application module 320 then makes an instance specific copy of the parameterizable command line string. Figure 4A is a table comprising one exemplary implementation of parameter application module 320 instructions for directing SoC netlist builder and verification computer system 100 to perform these design functions associated with the inclusion of a timer device in a desired design.